DIGITAL LOGIC TRAINER (CMOS)

MODEL - DLTDM101

This trainer has been designed with a view to provide practical and experimental knowledge of different digital CMOS Logics.



SPECIFICATIONS

| 1. 2. 3. | Power supply Built in Power Supply Clock Frequency | : | 230V Ac, + 10 % +5 V, 500mA regulated Variable from 1 Hz to 1 Khz |
|----------------|--|---|---|
| 4. | CMOS Logics provided | : | Multi vibrator D/JK Flip Flop Counter Operational Amplifier Phase Locked Loop Frequency Modulation/Demodulation Memory System |
| 5. | High /Low switches | : | 8 Nos provided to apply 0 and 1 level |
| 6. | LEDs | : | 8 Nos provided for output/input level |
| 7. | Displays | : | 1 nos. Seven segment LED display with decoder IC for BCD input. |
| 8. | Terminals | : | All terminals (2mm sockets) soldered Properly to avoid loose connections. |

9. All ICs provided on sockets and possible to replace without opening the trainer board.

- 10. All Ics are visible from top and the pin extensions are available around ICs with diagrams.
- 11. Standard 2 mm sockets are provided with 20 Nos of rugged connecting leads in assorted colours for Making connections.
- 12. Standard Accessories

SIGMA

: 1. A Instructional Manual. - 1 No. 2. Connecting wires - 20 Nos. In assorted colours.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

| Sigma Trainers E-103, Jai Ambe Nagar, | Phone Fax ISDN | : 079-26852427 / 26767512 : 079-26840290 / 26759661 : 079-26859162 / 26853758 | Dealer:- |
|--|----------------------|---|----------|
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